Tentative Schedule

High End Workshop on AI/ML Algorithms and Applications in VLSI Design and Technology

(Under the KARYASHALA Scheme – A SERB initiative)

17th – 22nd July 2023, DA-IICT Gandhinagar, Gujarat, India

Date	08:30 am to 09:00 am	09:00 am to 09:45 am	09:45 am to 10:15 am	Slot-1 10:15 am to 11:30 am	11:30 am to 11:45 am	Slot-2 11:45 am to 1:00 pm	01:00 pm to 2:00 pm	Slot-3 2:00 pm to 5 pm
17th July 2023 (Monday)	Registration (AIP Lab, Lab Building, Ground Floor)	Inauguration (AIP Lab, Lab Building, Ground Floor)	High Tea	Applying Computational Intelligence and Machine Learning to address Computational Problems in VLSI Systems (Dr. Jai Narayan Tripathi, IIT Jodhpur)	Tea & Snacks	Applying Computational Intelligence and Machine Learning to address Computational Problems in VLSI Systems (Dr. Jai Narayan Tripathi, IIT Jodhpur)	Lunch	Lab on Computational Intelligence and Machine Learning in VLSI (Dr. Jai Narayan Tripathi, IIT Jodhpur)

Date	Slot-1 10:00 am to 11:15 am	11:15 am to 11:30 am	Slot-2 11:30 am to 12:45 pm	12:45 pm to 2:00 pm	Slot-3 2:00 pm to 3:30 pm	03:30 pm to 03:45 pm	Slot-4 03:45 pm to 05:00 pm
18th July 2023 (Tuesday)	Machine Learning and Overview (Dr. M. V. Joshi, DA-IICT Gandhinagar)	Tea & Snacks	Deep Learning: Conceptual and Practical Guide (Dr. Bakul Gohel, DA-IICT Gandhinagar)	Lunch	Lab on Machine Learning and Deep Learning Dr. M.V. Joshi and Dr. Bakul, DA-IICT Gandhinagar)	Tea & Snacks	Lab on Machine Learning and Deep Learning (Dr. M.V. Joshi and Dr. Bakul, DA-IICT Gandhinagar)
19th July 2023 (Wednesday)	Applications of AI/ML in Embedded and VLSI Design (Dr. Vinay Palaparthy, DA-IICT Gandhinagar)		Applications of AI/ML in Embedded and VLSI Design (Dr. Vinay Palaparthy, DA-IICT Gandhinagar)		Lab on Exploratory Data Analysis (Dr. Vinay Palaparthy, DA-IICT Gandhinagar)		Lab on Exploratory Data Analysis (Dr. Vinay Palaparthy, DA-IICT Gandhinagar)
20th July 2023 (Thursday)	Hardware Design for AI/ML (Dr. Binod Kumar, IIT Jodhpur)		Hardware Design for AI/ML (Dr. Binod Kumar, IIT Jodhpur)		Hardware Design Implementation of AI/ML Applications (Dr. Binod Kumar, IIT Jodhpur)		Hardware Design Implementation of AI/ML Applications (Dr. Binod Kumar, IIT Jodhpur)
21st July 2023 (Friday)	Machine Learning based Analysis of VLSI Signal and Power Integrity Challenges (Dr. Kavicharan Mummaneni, NIT Silchar)		Machine Learning based Analysis of VLSI Signal and Power Integrity Challenges (Dr. Kavicharan Mummaneni, NIT Silchar)		VLSI and on-chip Interconnect System (Dr. Yash Agrawal, DA-IICT Gandhinagar)		Lab on VLSI Interconnect System Design using AI/ML (Dr. Yash Agrawal, DA-IICT Gandhinagar)
22nd July 2023 (Saturday)	Spintronics – Perspectives and Challenges (Dr. B. K. Kaushik, IIT Roorkee)		Quiz and Award		Valedictory		